**MARC2 DE2 Pinout Data**

|  |  |  |  |
| --- | --- | --- | --- |
| **Signal** | **PIN** | **Device Name** | **Function** |
| Reset | G26 | Key0 | Reset CPU |
| Run | N25 | SW0 | Allow CPU to run code |
| Clock | N2 | Clock\_50 | Onboard 50 MHz clock |
| LCD\_data(0) | J1 |  |  |
| LCD\_data(1) | J2 |  |  |
| LCD\_data(2) | H1 |  |  |
| LCD\_data(3) | H2 |  |  |
| LCD\_data(4) | J4 |  |  |
| LCD\_data(5) | J3 |  |  |
| LCD\_data(6) | H4 |  |  |
| LCD\_data(7) | H3 |  |  |
| LCD\_E | K3 |  | LCD enable |
| LCD\_ON | L4 |  |  |
| LCD\_RS | K1 |  | LCD register select |
| Reset\_LED | AD12 | LED R17 |  |
| Src\_LED | AE12 | LED R16 |  |
| LCD\_RW | K4 |  | LCD read/write |
| Reg\_select(2) | V2 | SW17 | Select output register for viewing |
| Reg\_select(1) | V1 | SW16 |  |
| Reg\_select(0) | U4 | SW15 |  |
|  |  |  |  |
|  |  |  |  |

Memory Map:

* LCD address = $0800
* ROM = $0000 to $01FF (1 kB words, 16 bit words, word addressable)
* RAM = $0200 to $07FF (4 kB words, 16 bit words, word addressable)